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PATENT APPLICATION

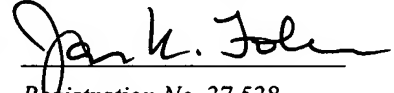
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hideaki Takizawa et al.
Serial No.: 10/660,053
Conf. No.: 7258
Filed: 9/11/2003
For: THIN FILM TRANSISTOR
MATRIX DEVICE
Art Unit: 2823
Examiner: Nguyen, Khiem D.

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

March 14, 2006

Date


Registration No. 37,538
Attorney for Applicant(s)

AMENDMENT AFTER NOTICE OF ALLOWANCE UNDER 37 C.F.R. §1.312

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to issuance of the above-named allowed Application, please make the following amendment.

In the Title:

Please amend the title of the invention to --THIN FILM TRANSISTOR MATRIX
DEVICE--.